

FIG. 1



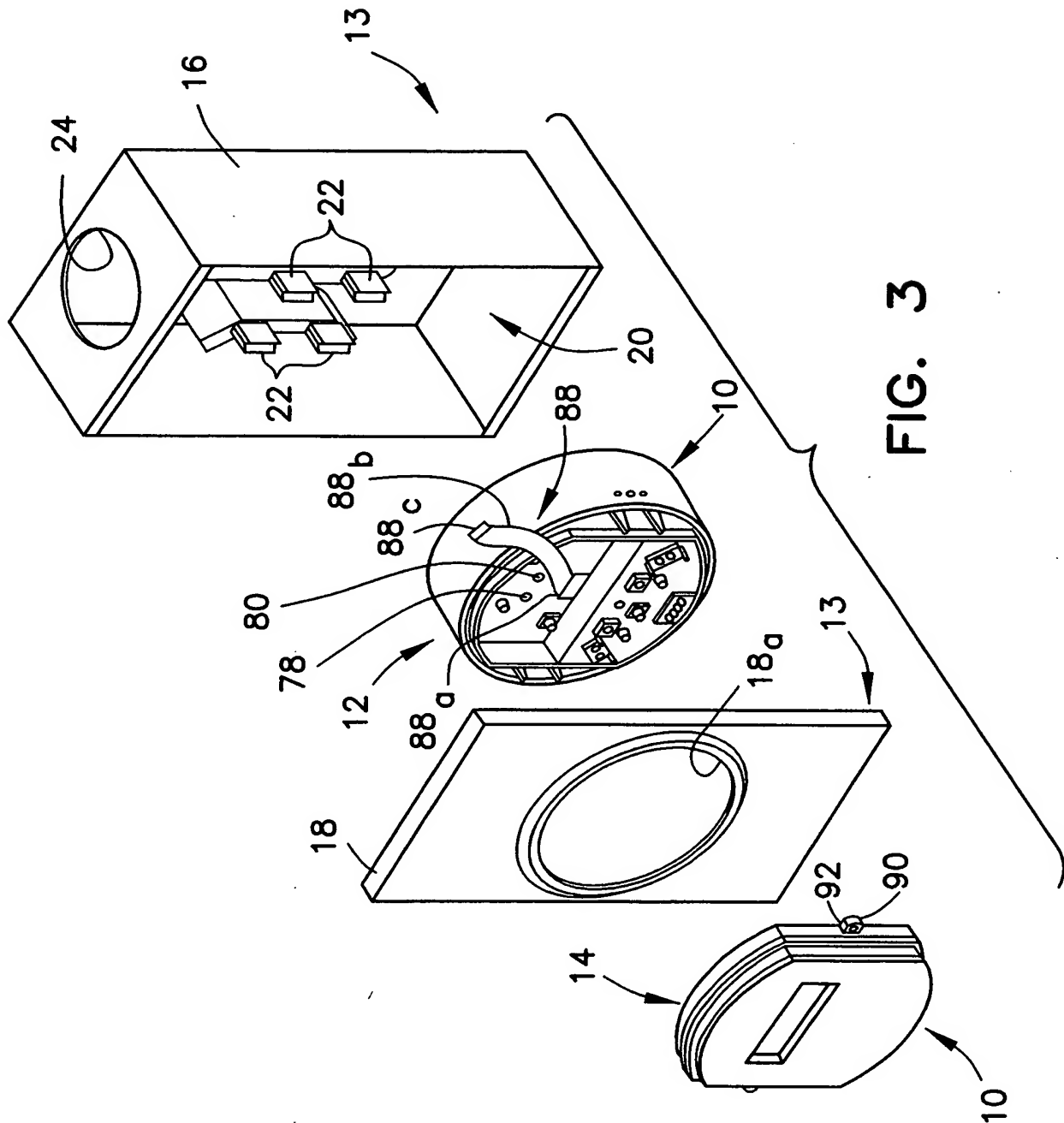


FIG. 3

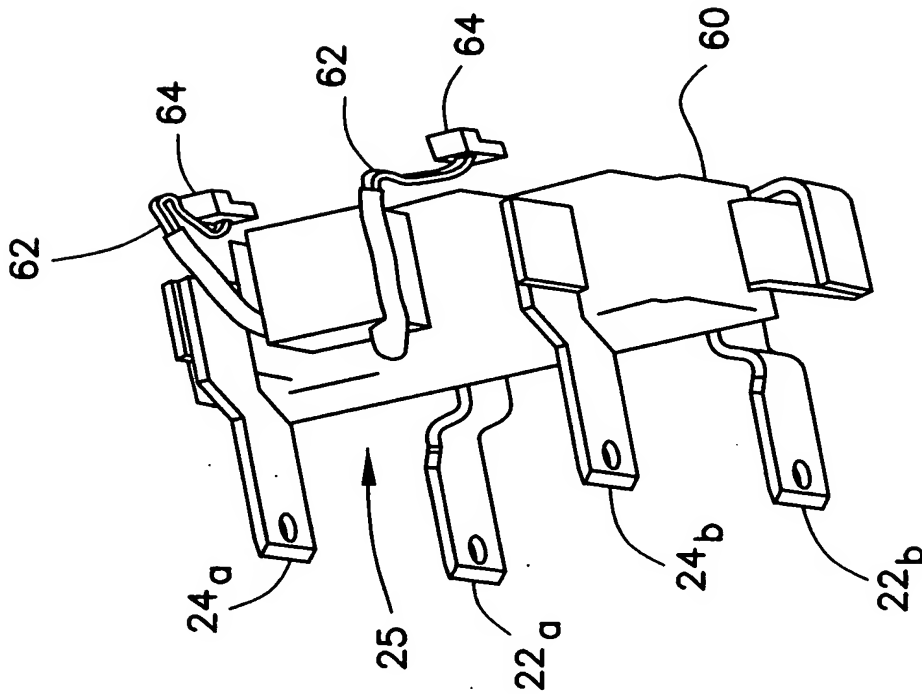


FIG. 4

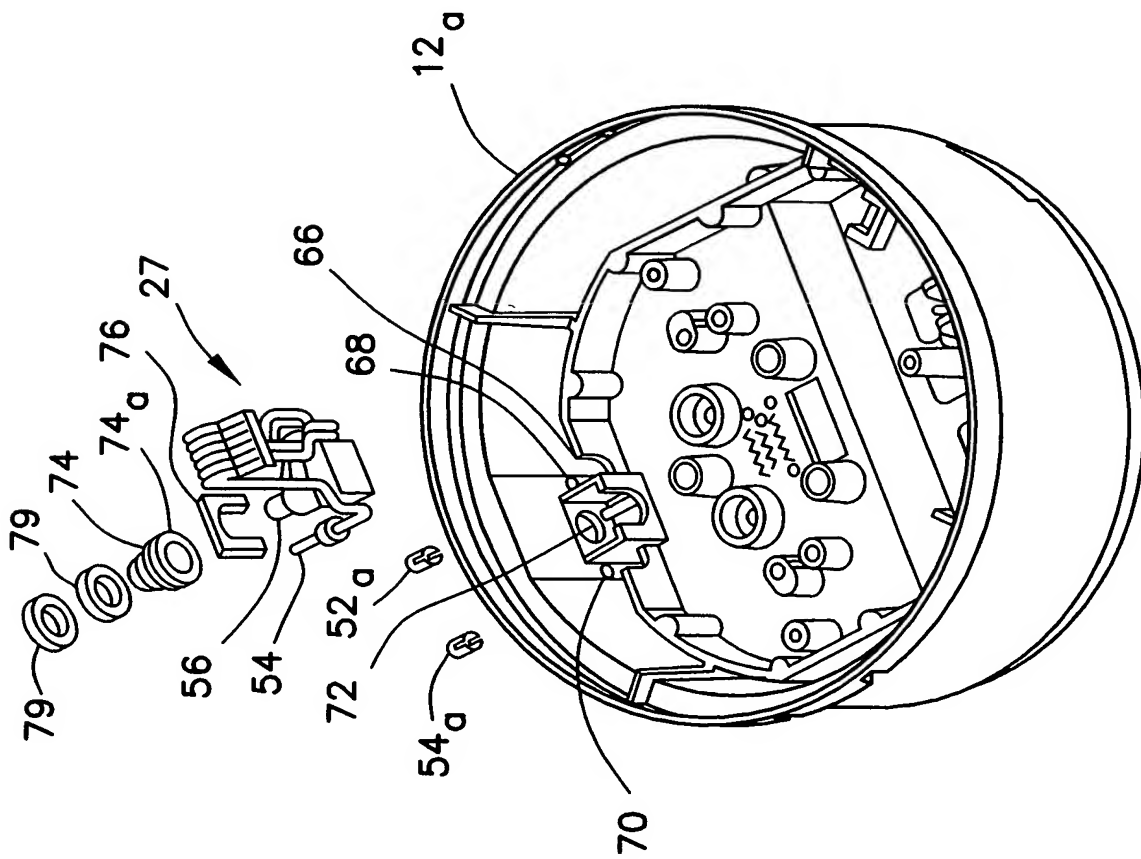


FIG. 5



ELECTRICALLY SAFE INTERFACE



FIG. 7

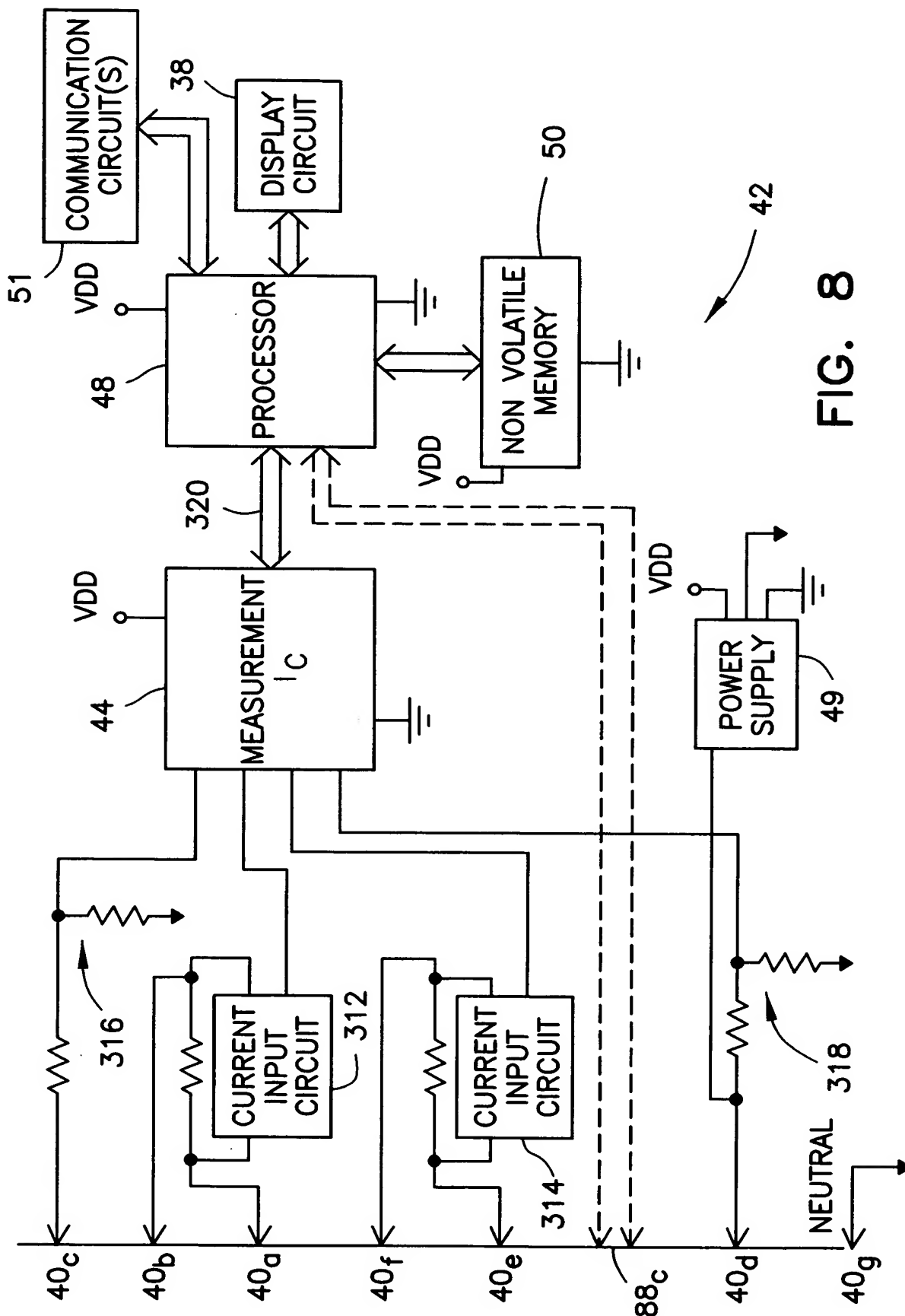


FIG. 8